

Fig.1 Composite Video Signal

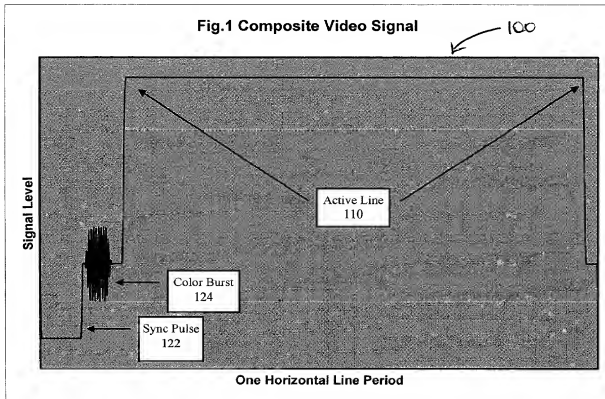


Fig. 2 VTR Modem Top-Level Block Diagram

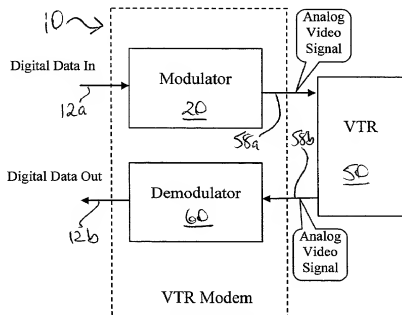


Fig. 3 Modulator Block Diagram

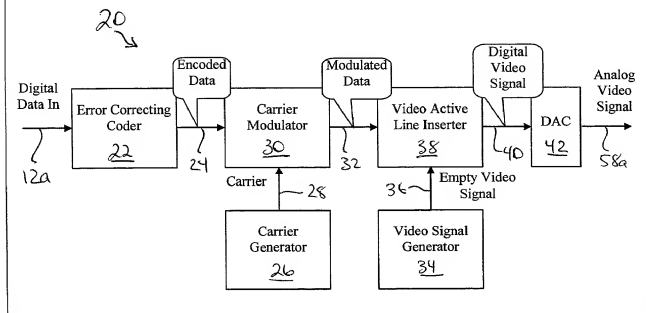


Fig. 4 Demodulator Block Diagram

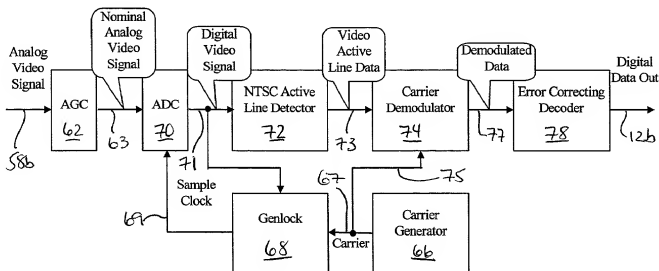


Fig. 4A Demodulator with Analog AGC

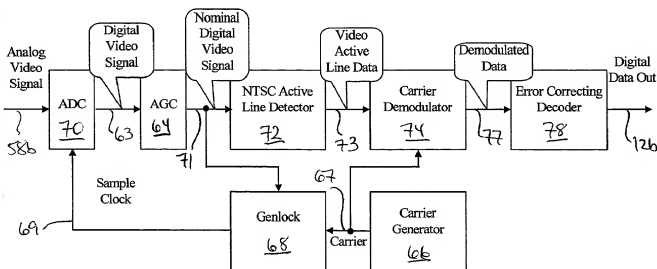


Fig. 4B Demodulator with Digital AGC

Fig. 5 Genlock Block Diagram

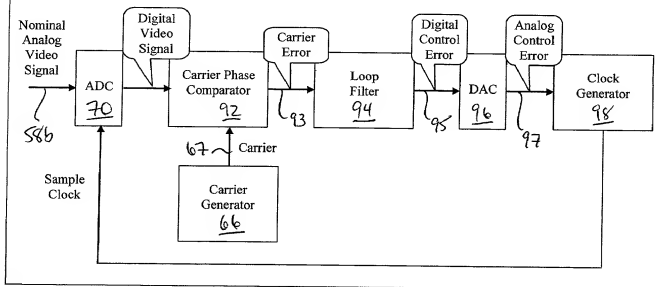
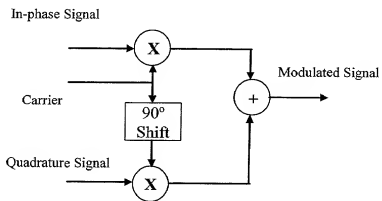
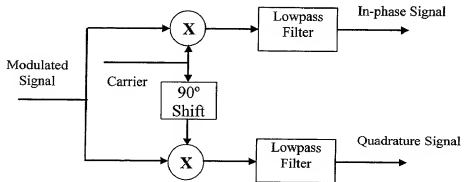


Fig. 6 Quadrature Amplitude Modulation and Demodulation



Modulation Method



Demodulation Method